

Fig 1

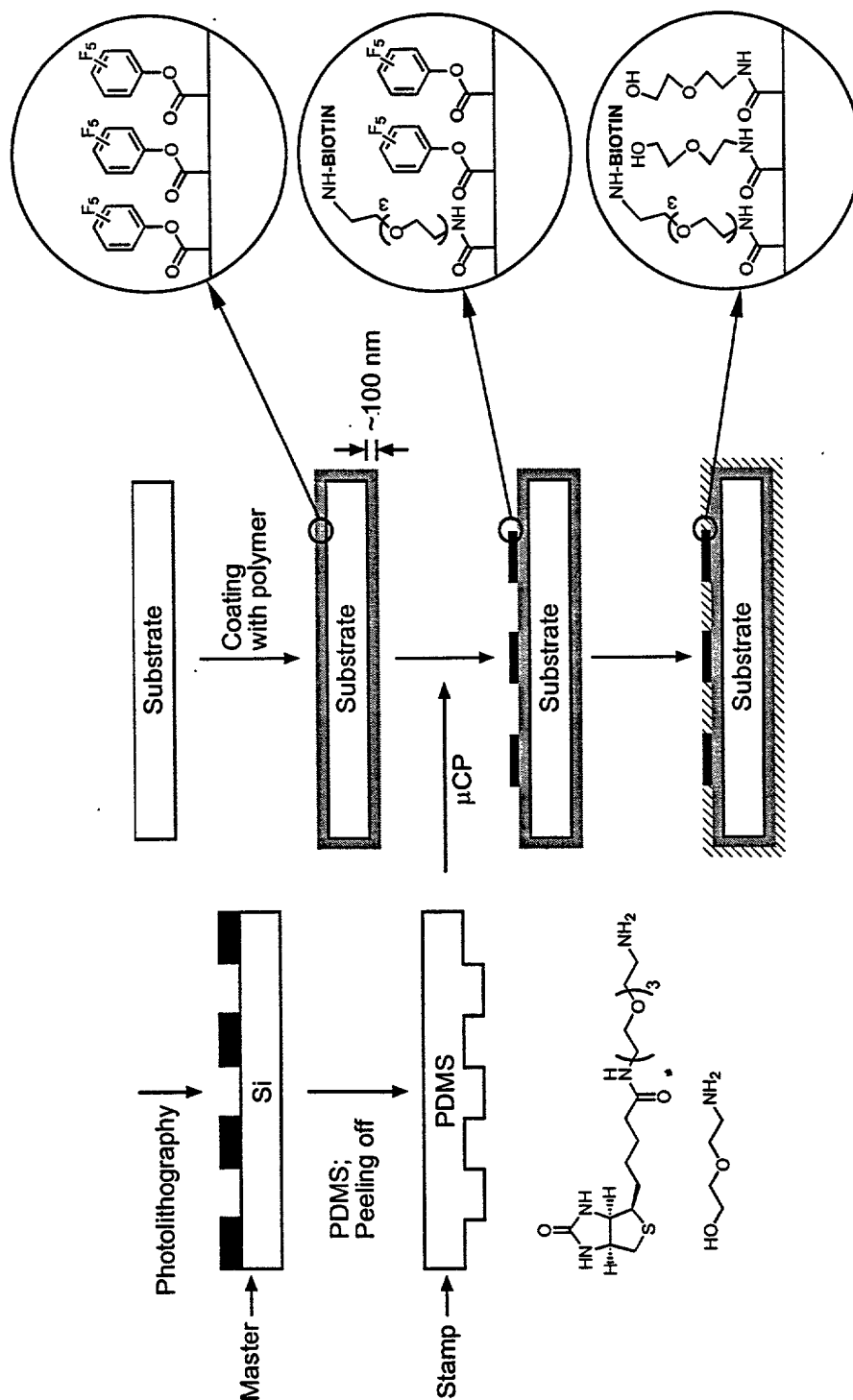
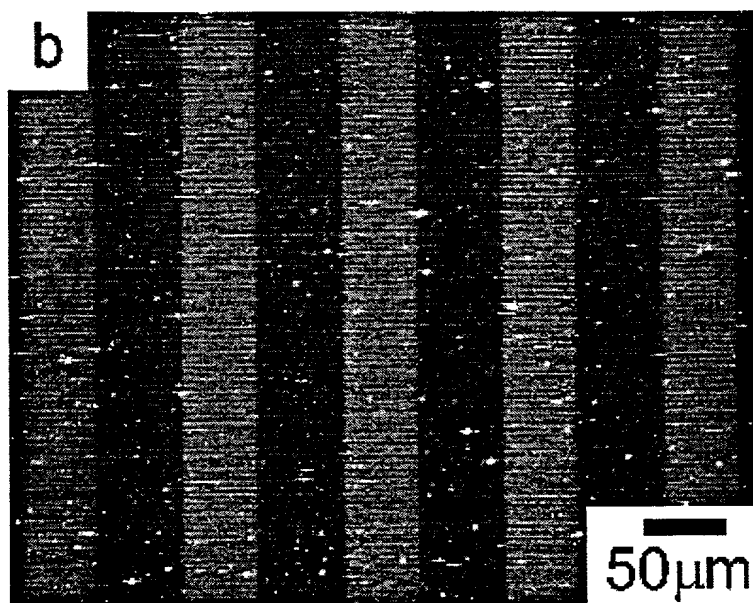
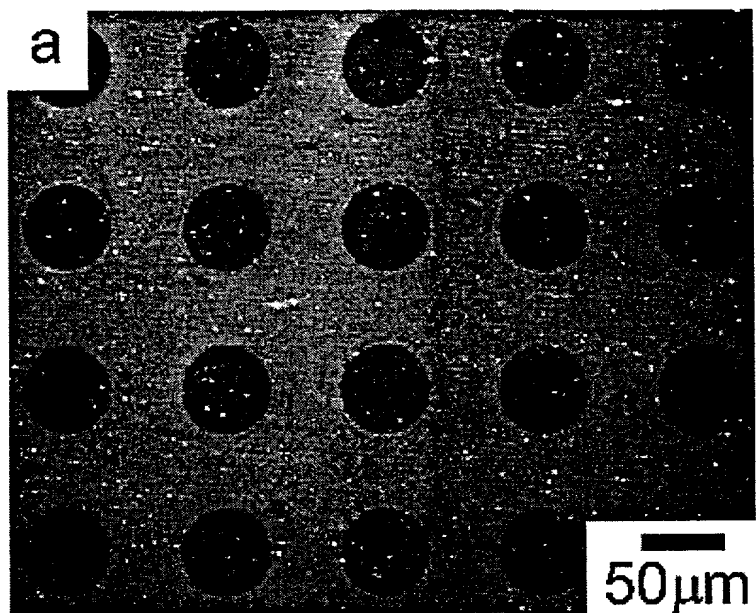


Fig 2



Figs 3a and 3b .

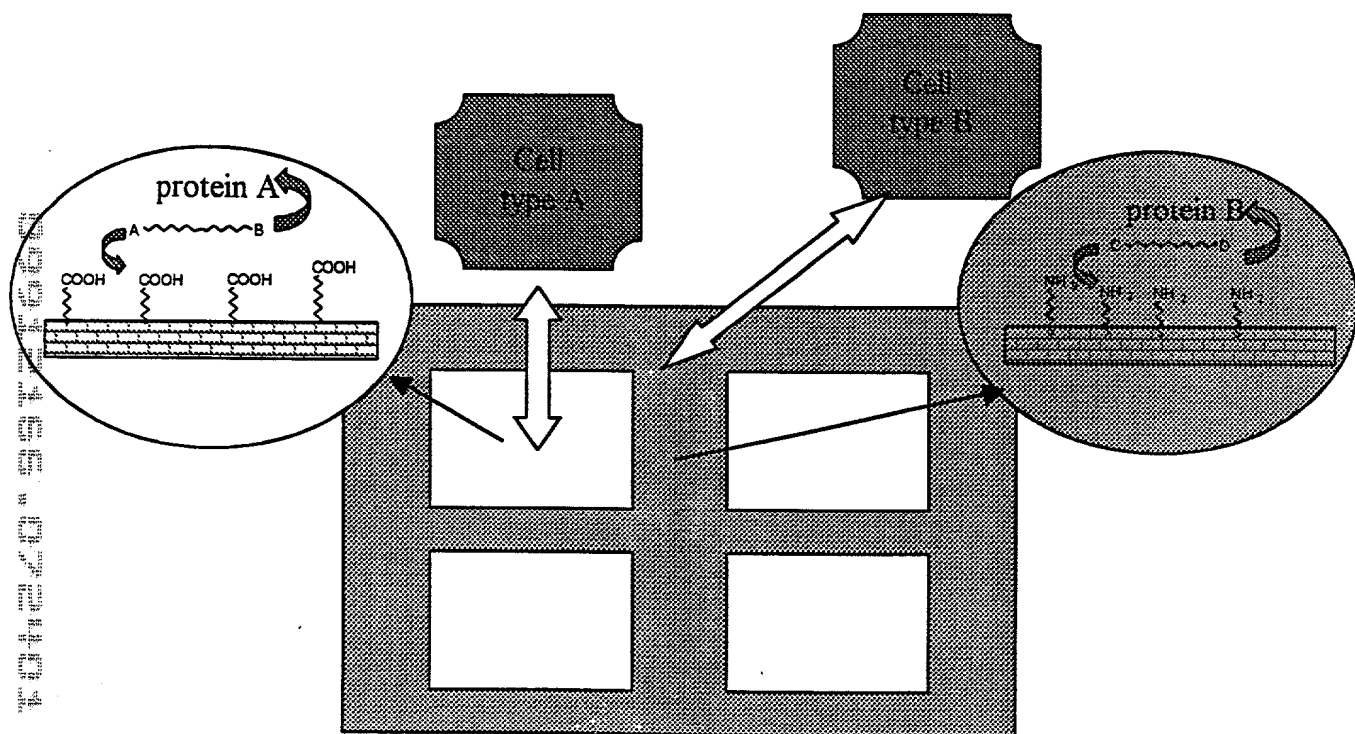


Fig 4

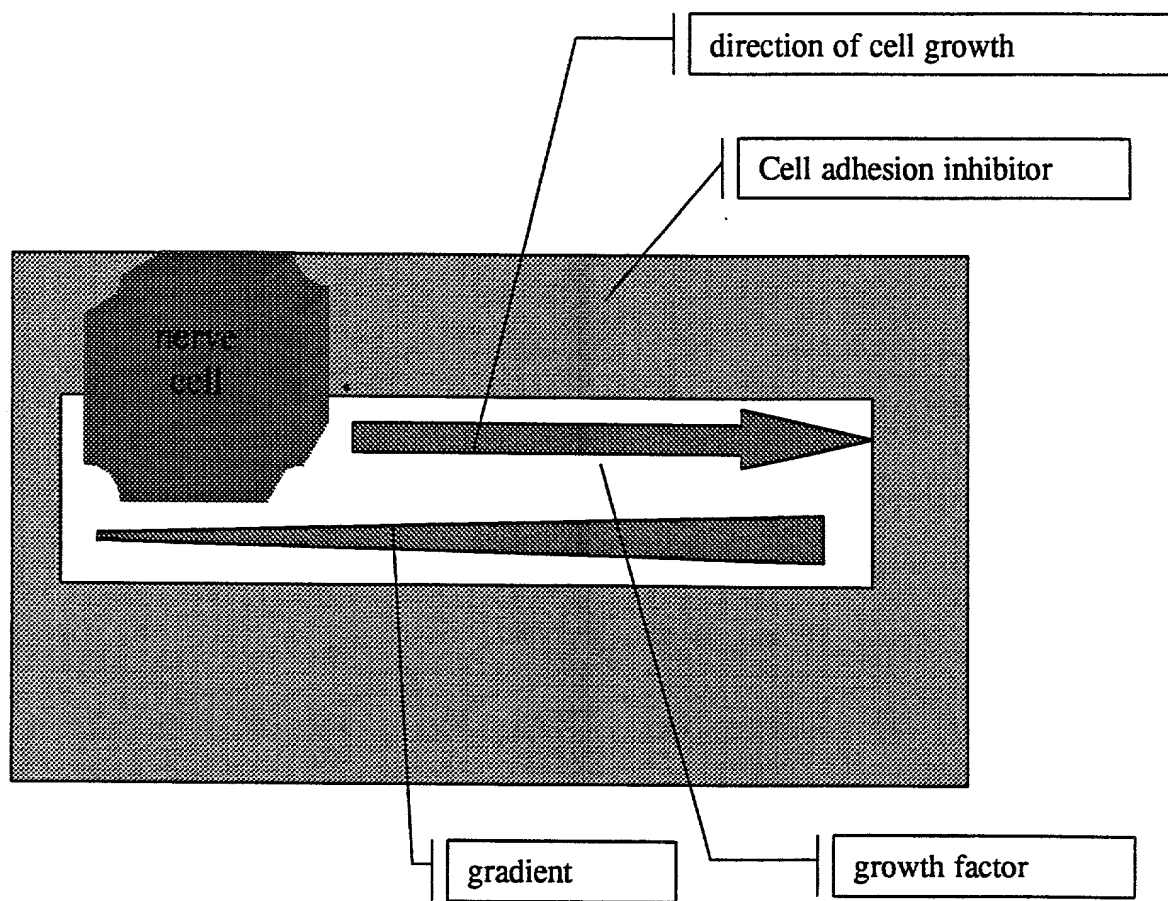
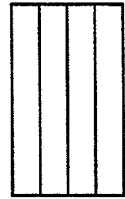
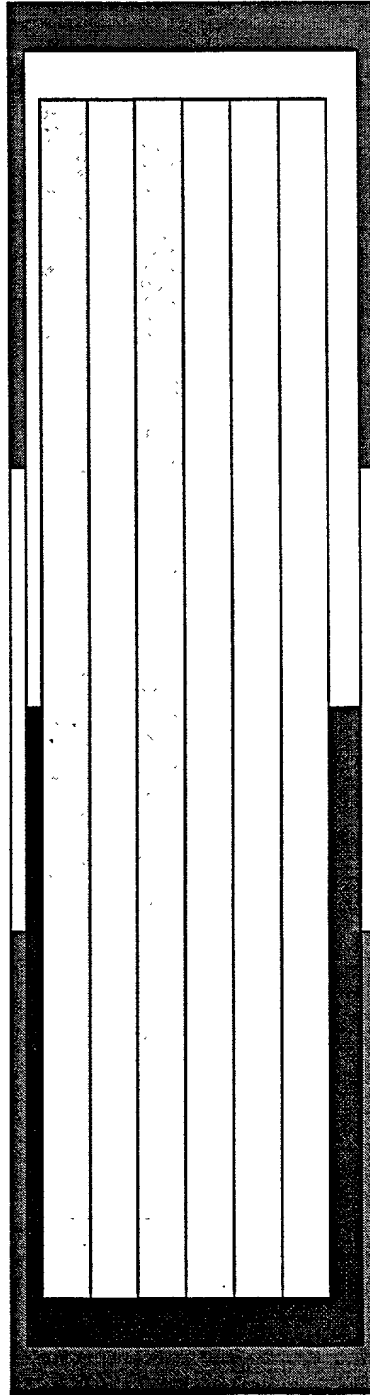
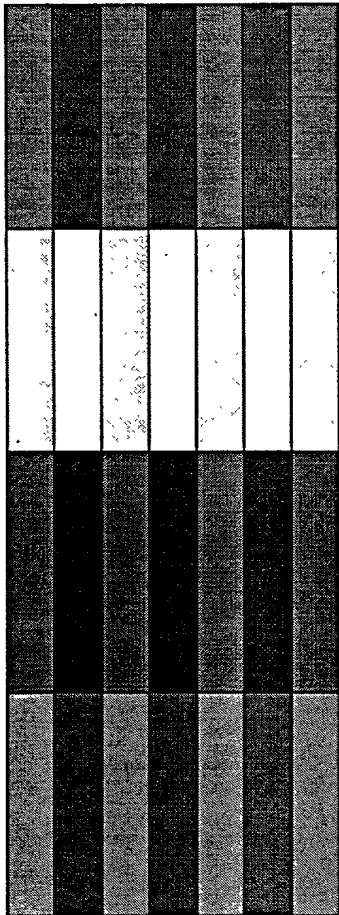


Fig 5

Fig. 6 is a plan view of the device showing the arrangement of the components in the device.



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Fig. 6

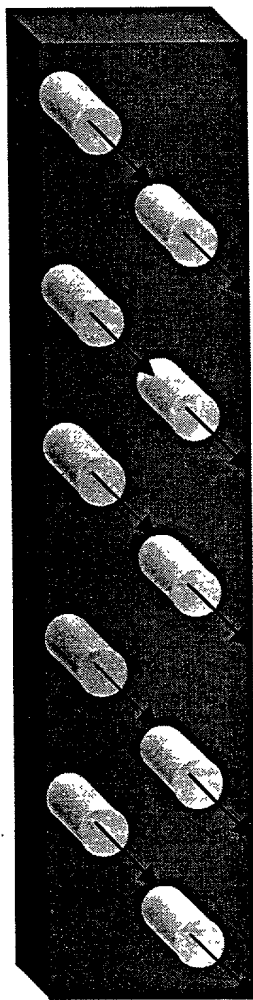
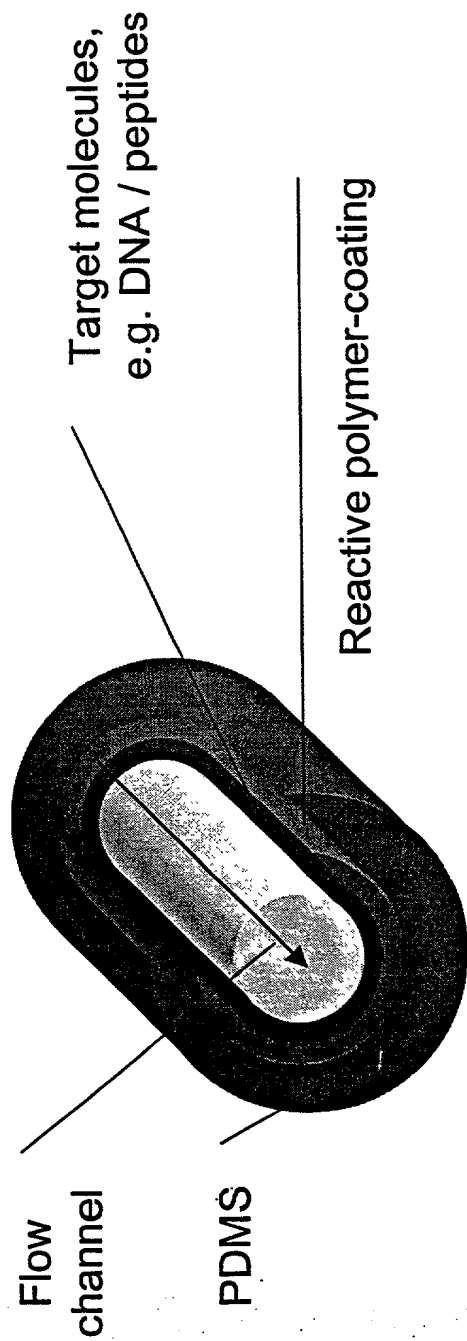


Fig 7